

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,251,618 B2  
APPLICATION NO. : 09/733372  
DATED : July 31, 2007  
INVENTOR(S) : Paul R. Petersen


Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Column 3, Line 48	"the 12C bus"	--the I2C bus--
Column 3, Lines 64-65	"memory controller 110"	--memory controller 108--
Column 4, Line 42	"200 may stored in"	--200 may be stored in--
Column 4, Line 43	"memory controller 110"	--memory controller 108--
Column 4, Line 47	"configuration data form"	--configuration data from--
Column 4, Lines 48-49	"memory controller 110"	--memory controller 108--

Signed and Sealed this

Eighth Day of April, 2008



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*